

„VALAHIA” UNIVERSITY OF TÂRGOVIȘTE

Faculty: Electrical Engineering, Electronics and Informational Technology

Master Timetable available 01.10.2018 – 19.10.2018

Study program: Integrated Electrical Systems Engineering in Vehicles

Year II, Semester I

Module: Numerical simulation of integrated embedded systems (NSIES) – 14 hours of course

Academic year 2018-2019

-28 hours of laboratory

19 – 20	18 – 19	17 – 18	16-17	Week	Date
	<b>Course</b> – Numerical simulation of integrated embedded systems A-302				1.10.2018 Monday
<b>Laboratory</b> – Numerical simulation of integrated embedded systems A-118					2.10.2018 Tuesday
	<b>Course</b> – Numerical simulation of integrated embedded systems A-302				3.10.2018 Wednesday
<b>Laboratory</b> – Numerical simulation of integrated embedded systems A-118					4.10.2018 Thursday
	Practice - Renault				5.10.2018 Friday
<b>Laboratory</b> – Numerical simulation of integrated embedded systems A-118		<b>Course</b> – Numerical simulation of integrated embedded systems A-302			8.10.2018 Monday
<b>Laboratory</b> – Numerical simulation of integrated embedded systems A-118					9.10.2018 Tuesday
<b>Laboratory</b> – Numerical simulation of integrated embedded systems A-118		<b>Course</b> – Numerical simulation of integrated embedded systems A-302			10.10.2018 Wednesday
<b>Laboratory</b> – Numerical simulation of integrated embedded systems A-118					11.10.2018 Thursday
	Practice - Renault				12.10.2018 Friday
<b>Course</b> – Numerical simulation of integrated embedded systems A-302					15.10.2018 Monday
<b>Laboratory</b> – Numerical simulation of integrated embedded systems A-118					16.10.2018 Tuesday
<b>Laboratory</b> – Numerical simulation of integrated embedded systems A-118					17.10.2018 Wednesday
					18.10.2018 Thursday
	Practice - Renault				19.10.2018 Friday

Professor of course – Associate professor **Otilia DRAGOMIR**

Professor of seminar - Associate professor **Otilia DRAGOMIR**